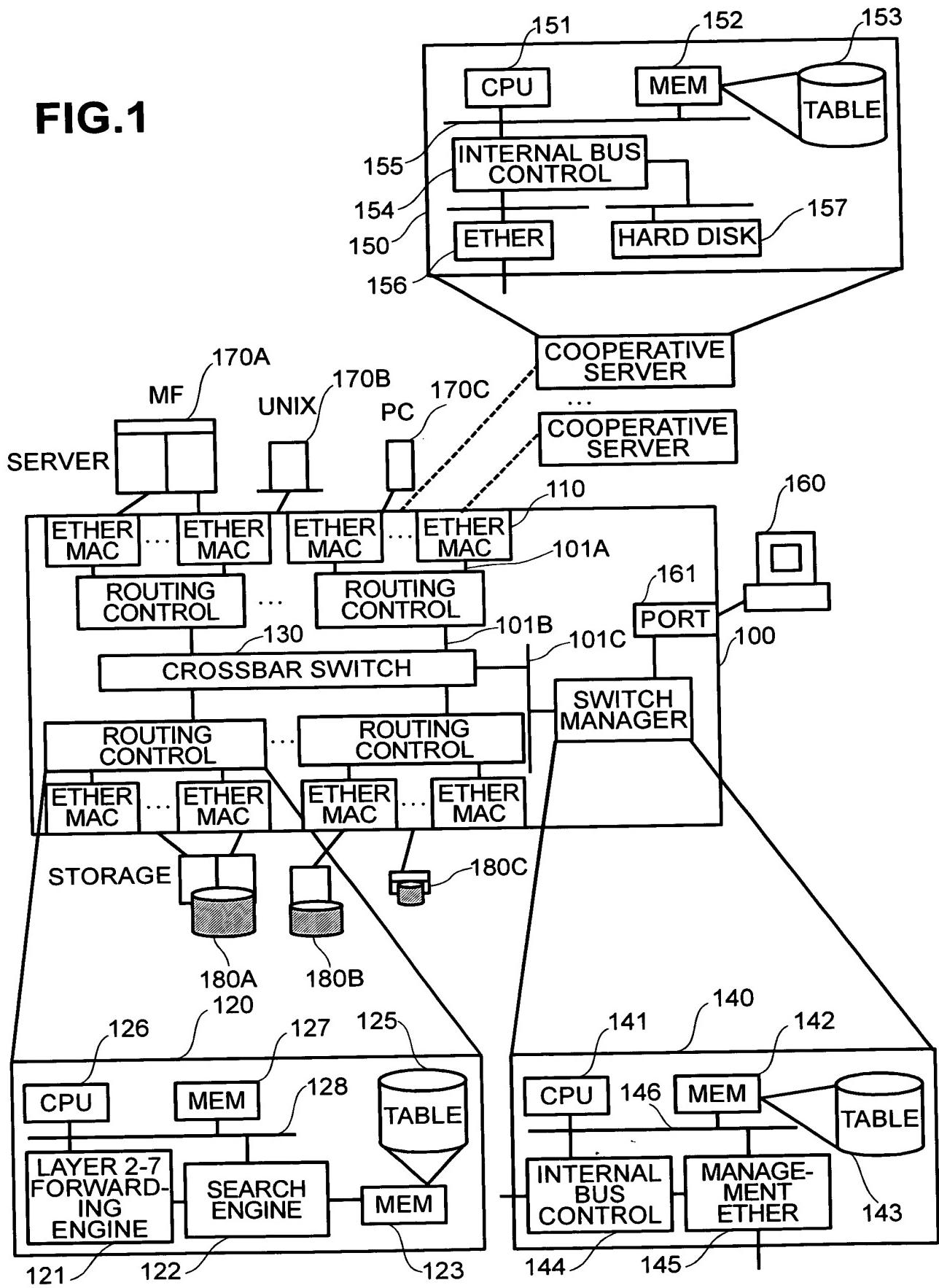
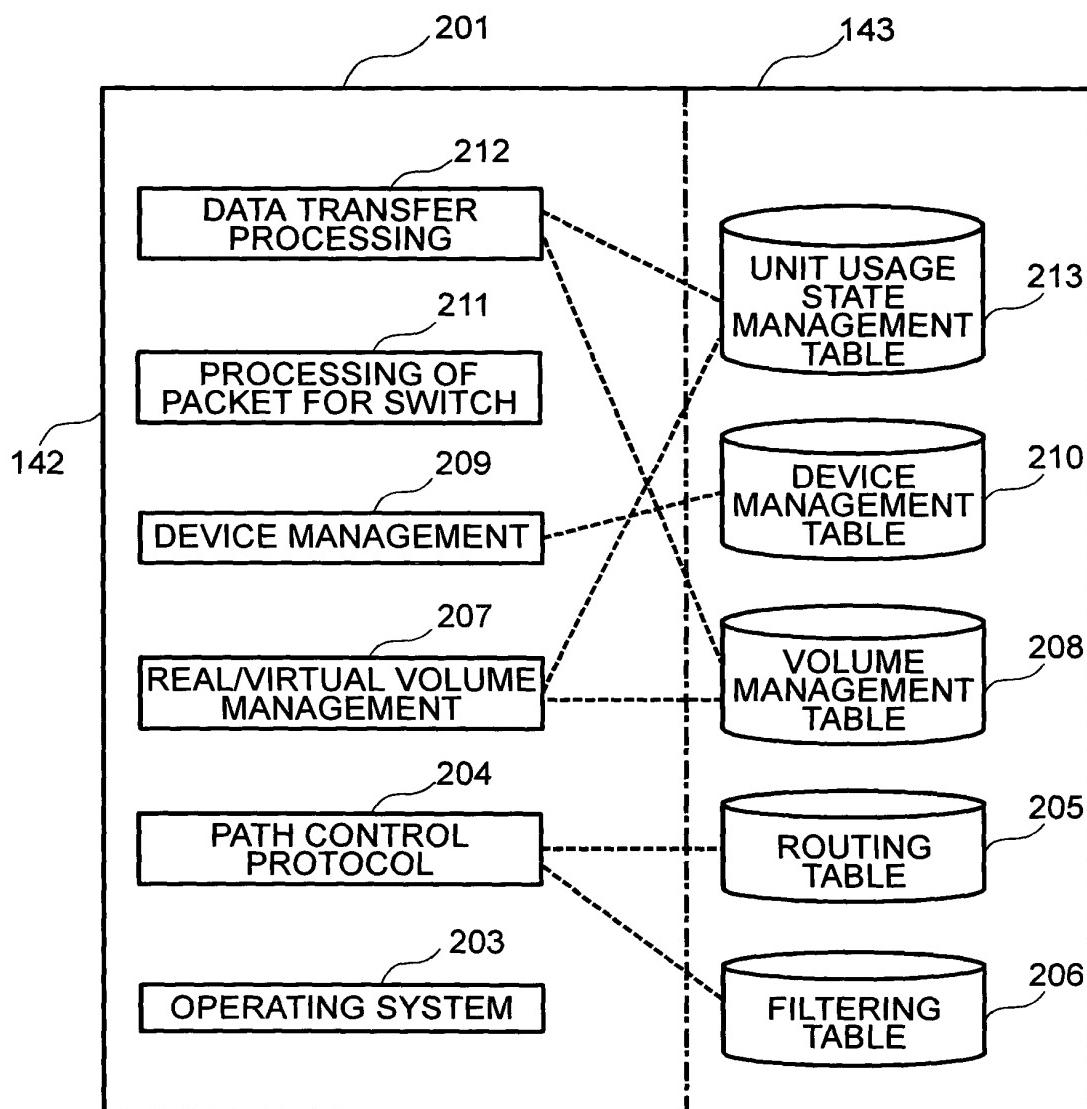


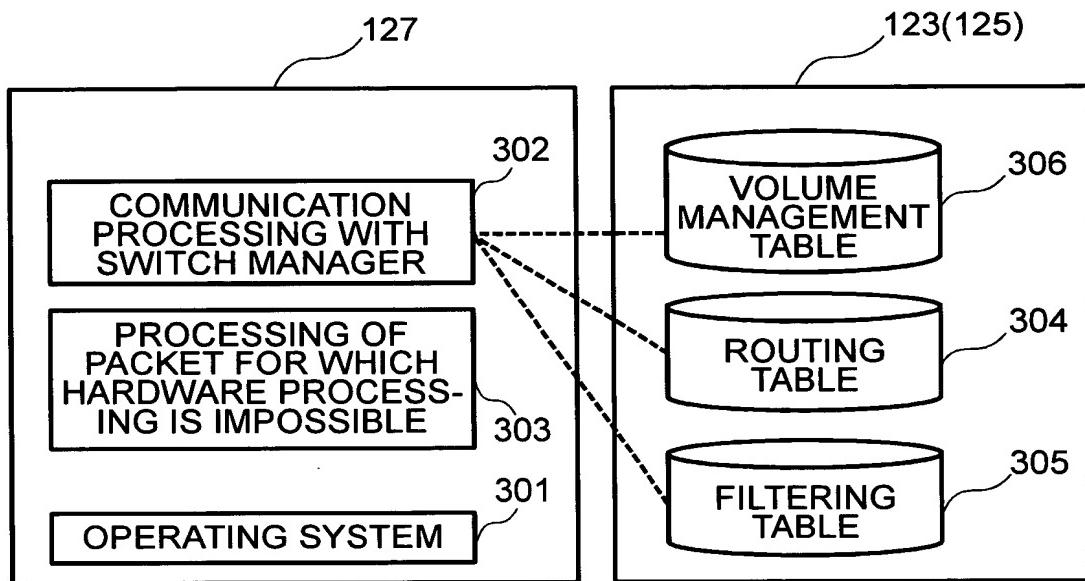
**FIG.1**



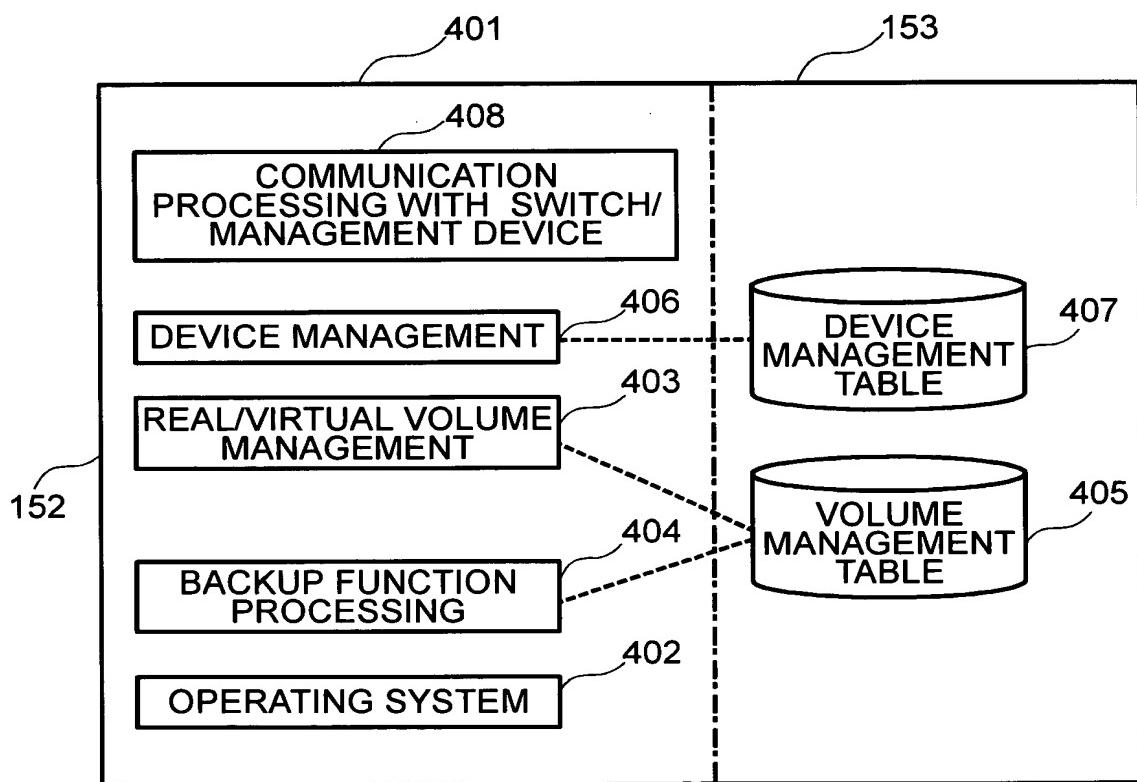
**FIG.2**



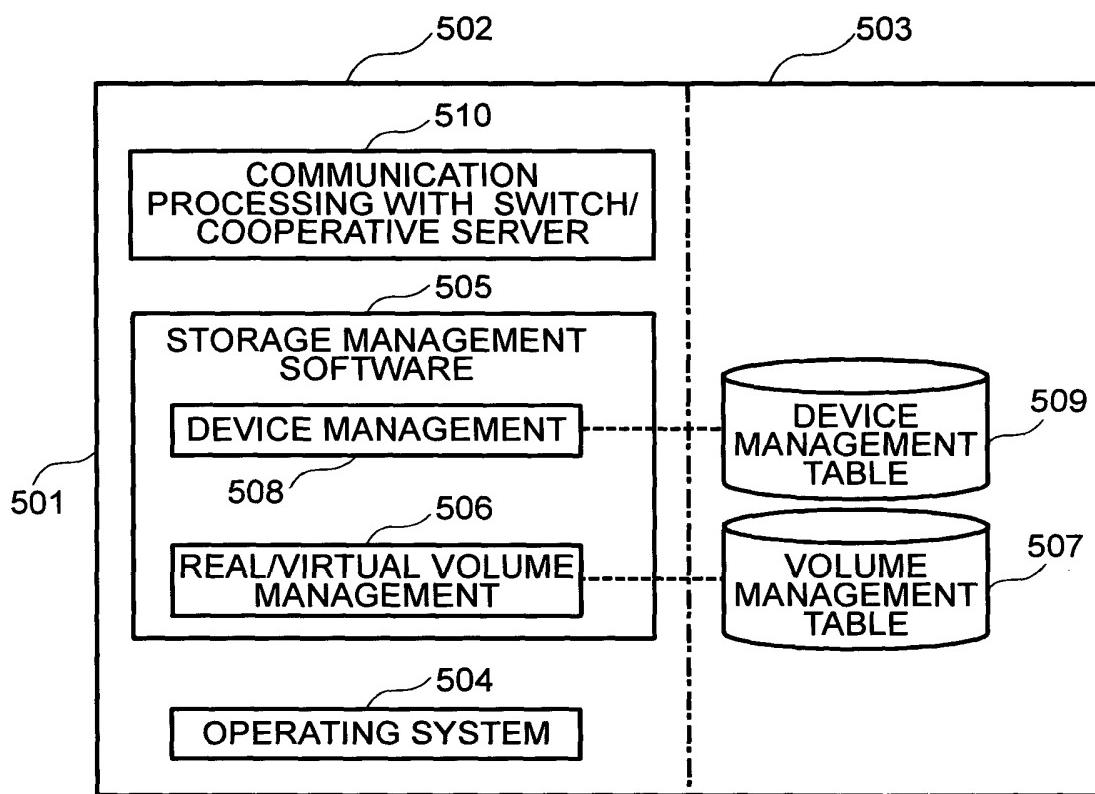
### FIG.3



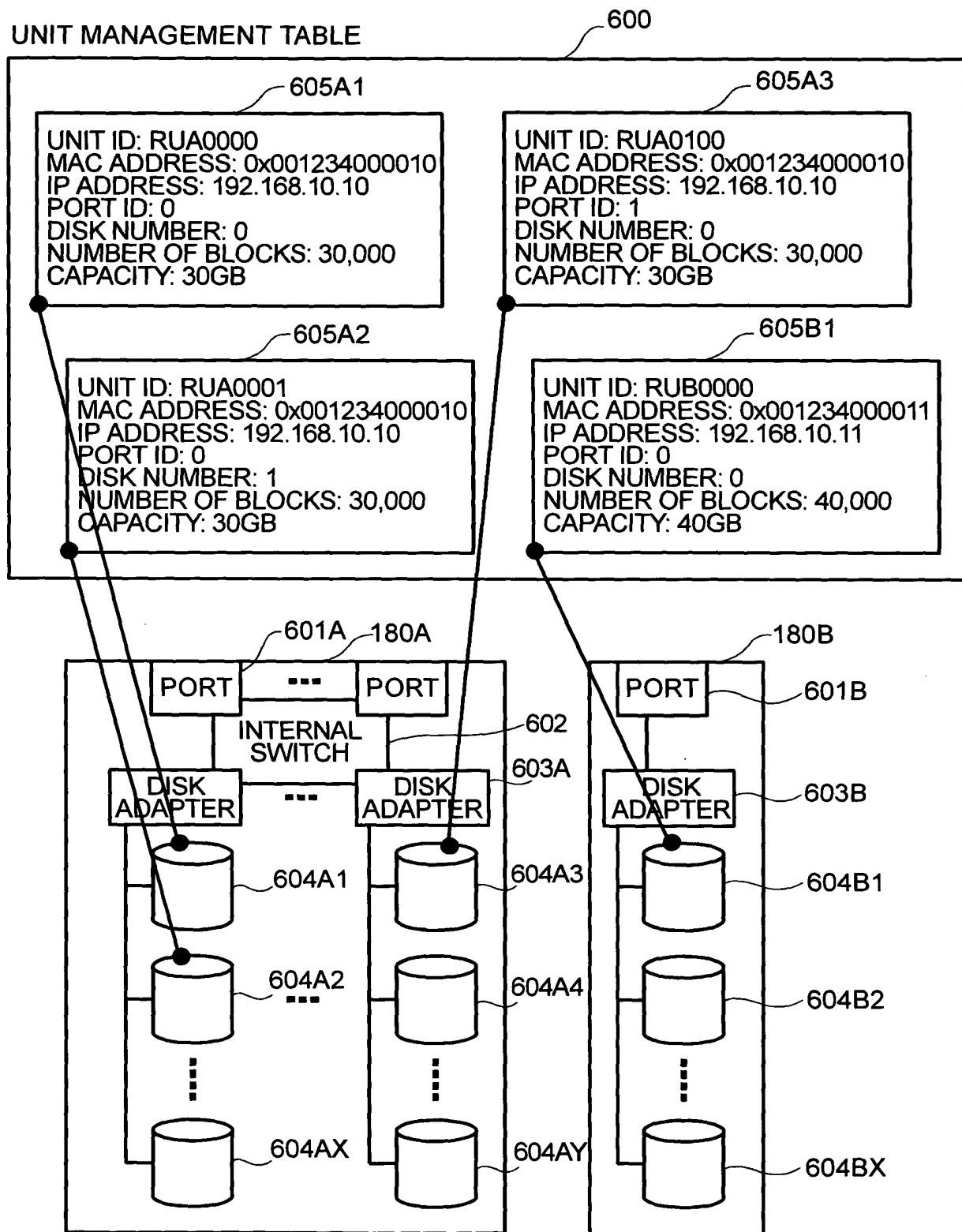
### FIG.4



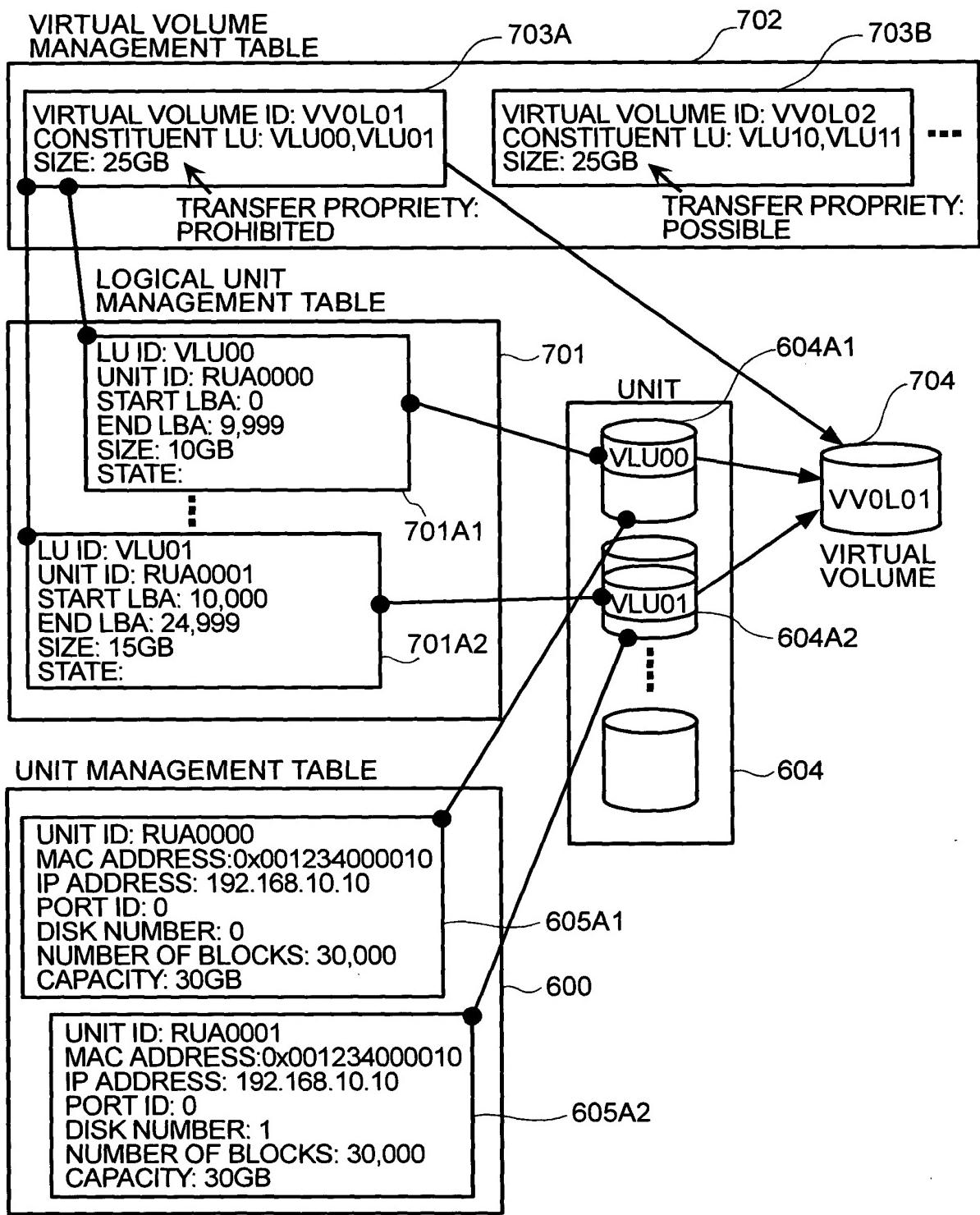
# FIG.5



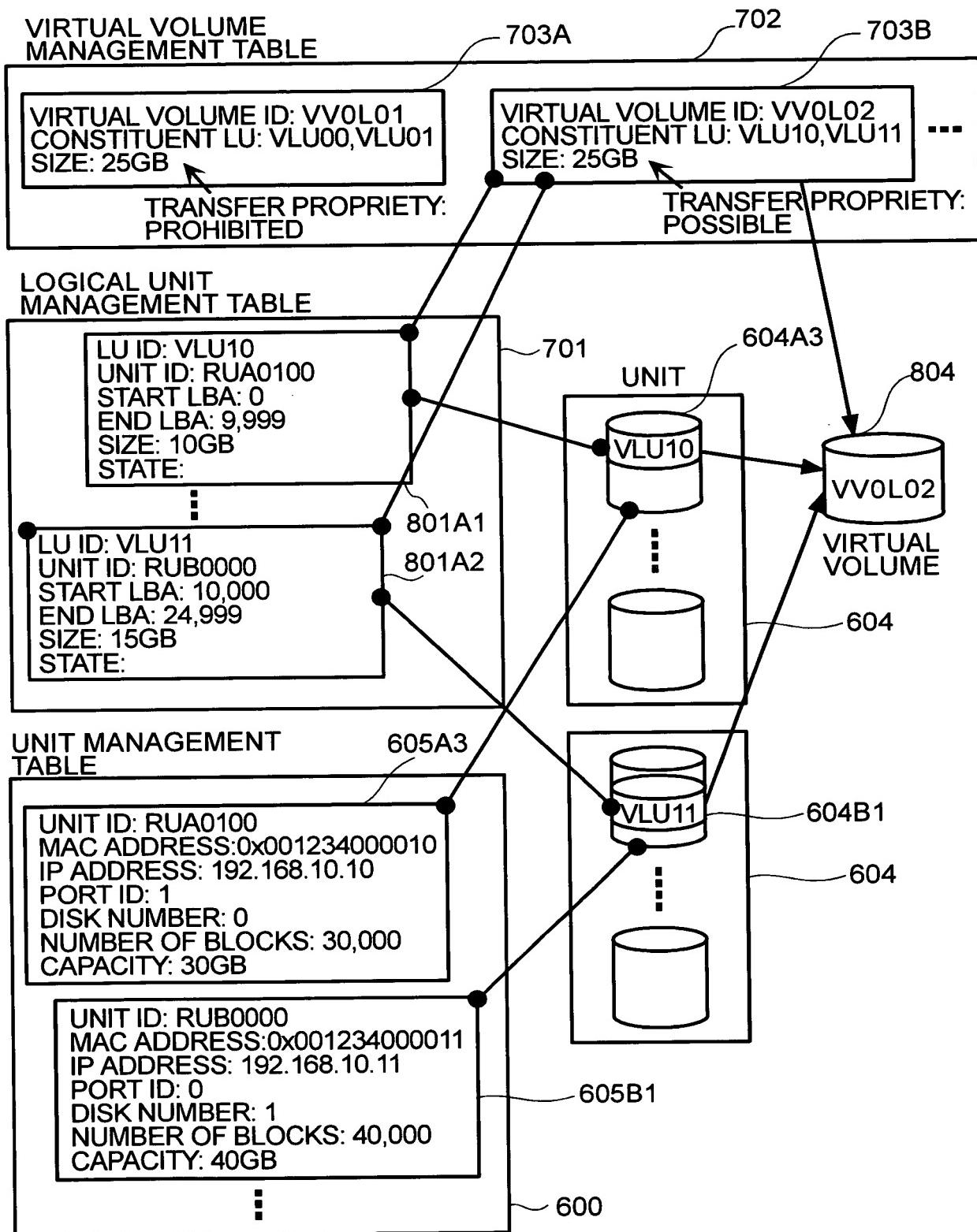
# FIG.6



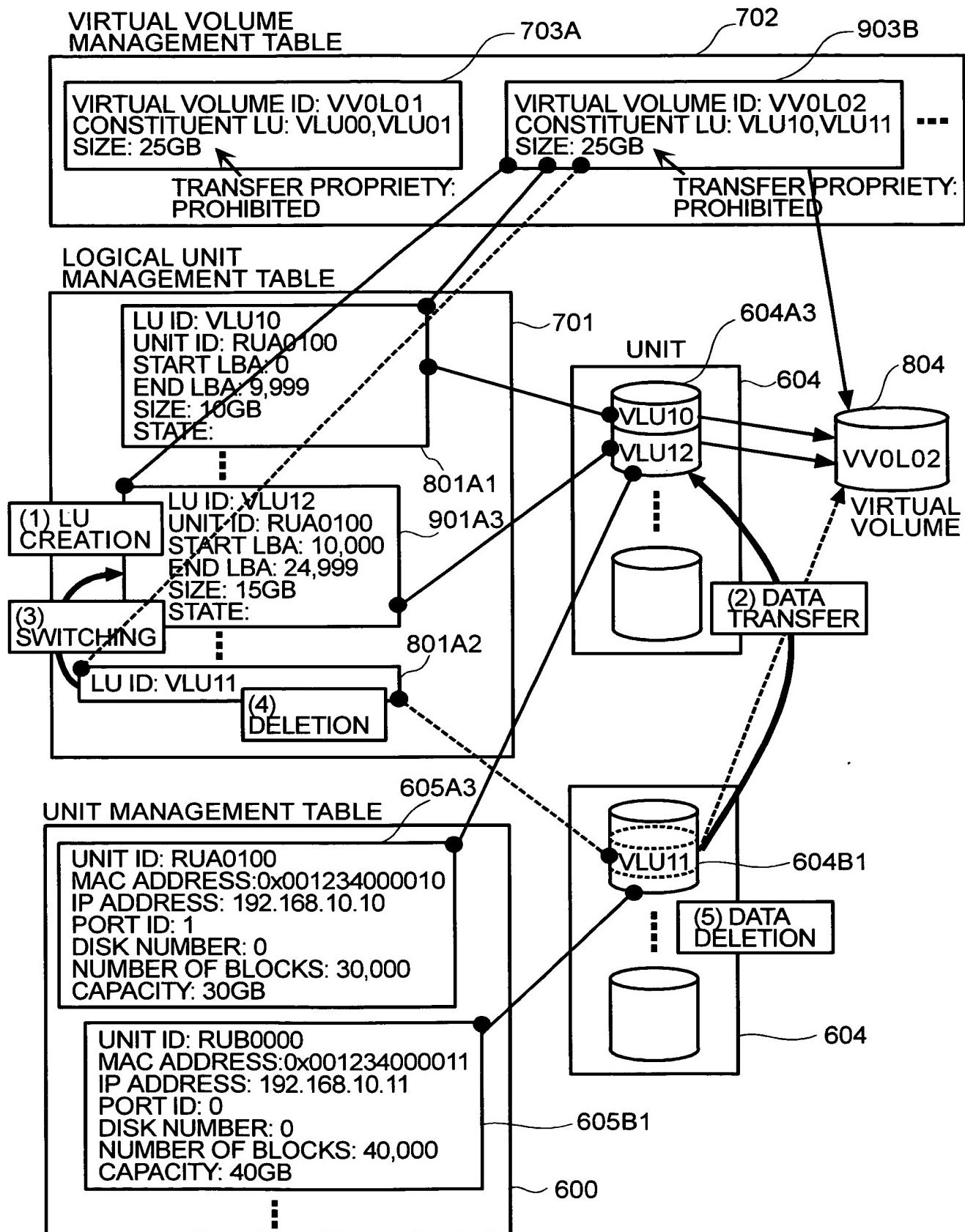
**FIG.7**



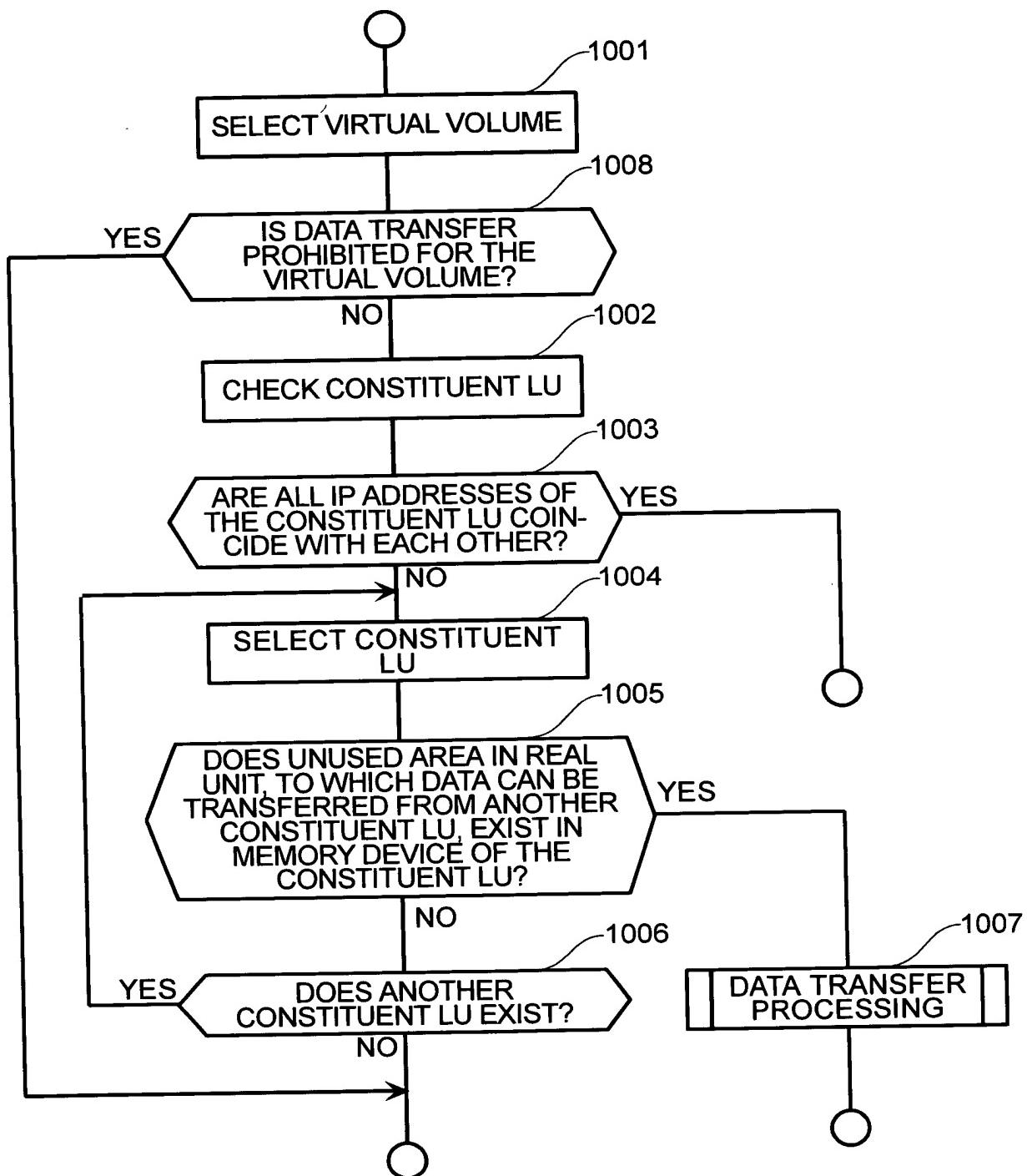
# FIG.8



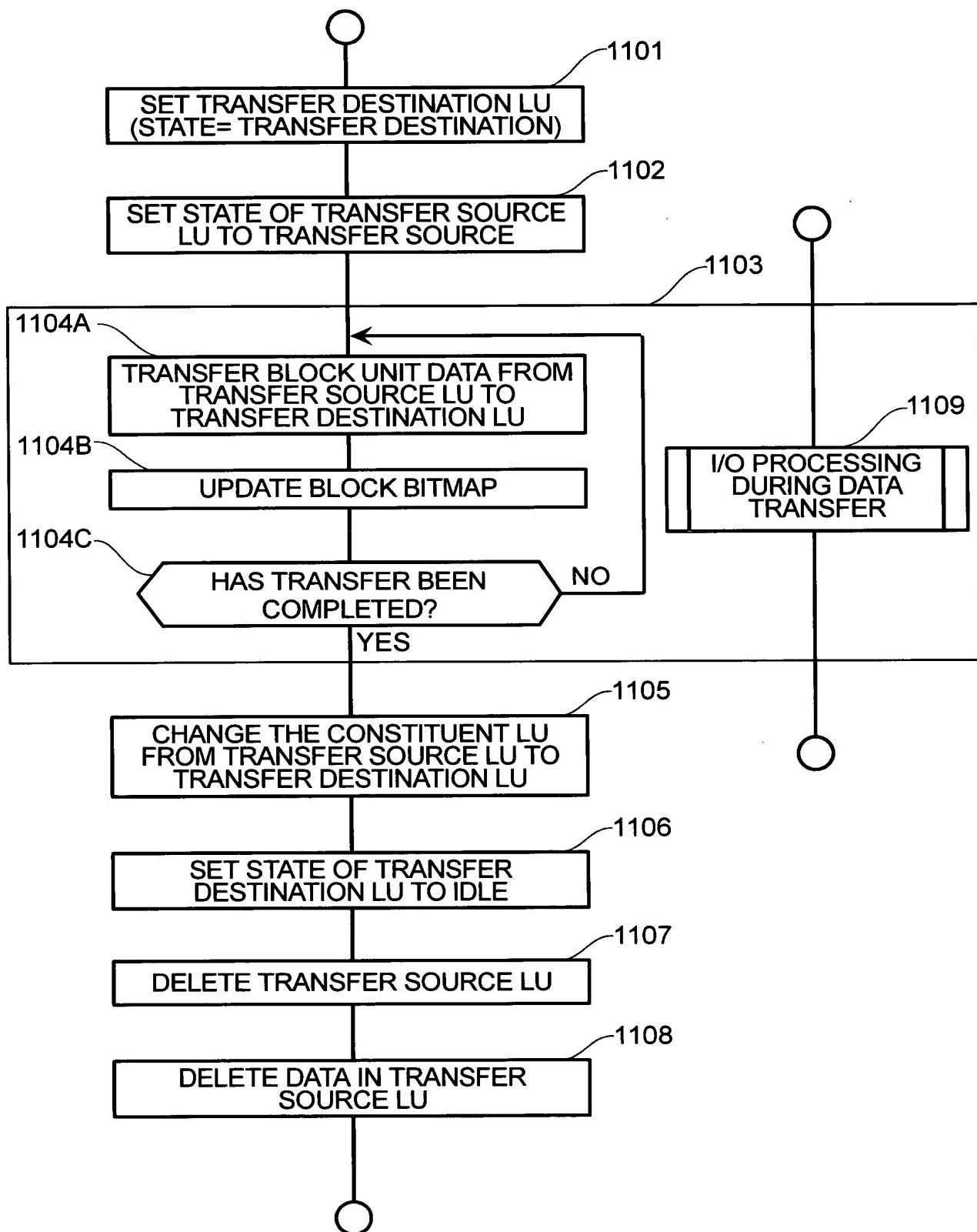
# FIG.9



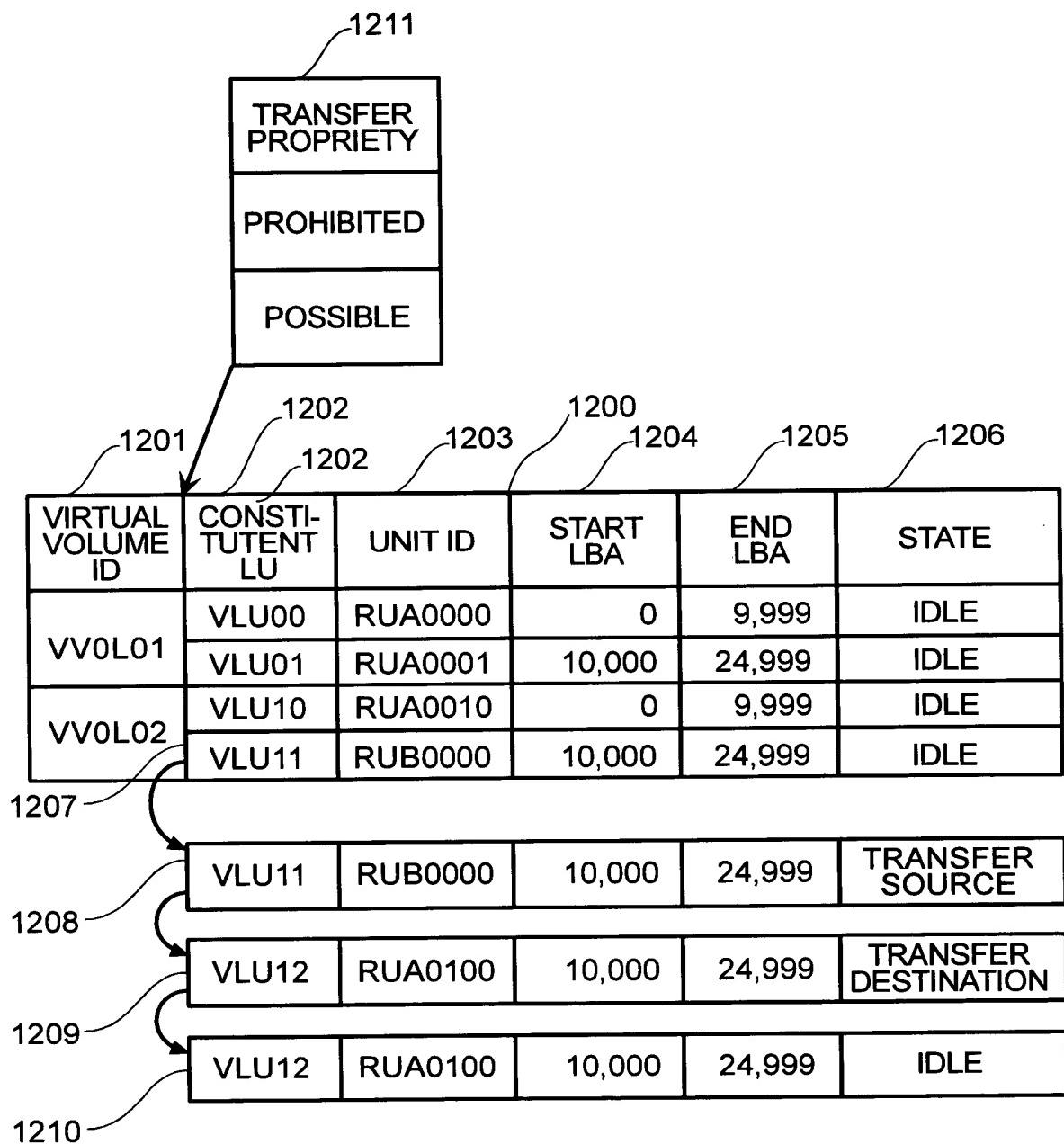
**FIG.10**



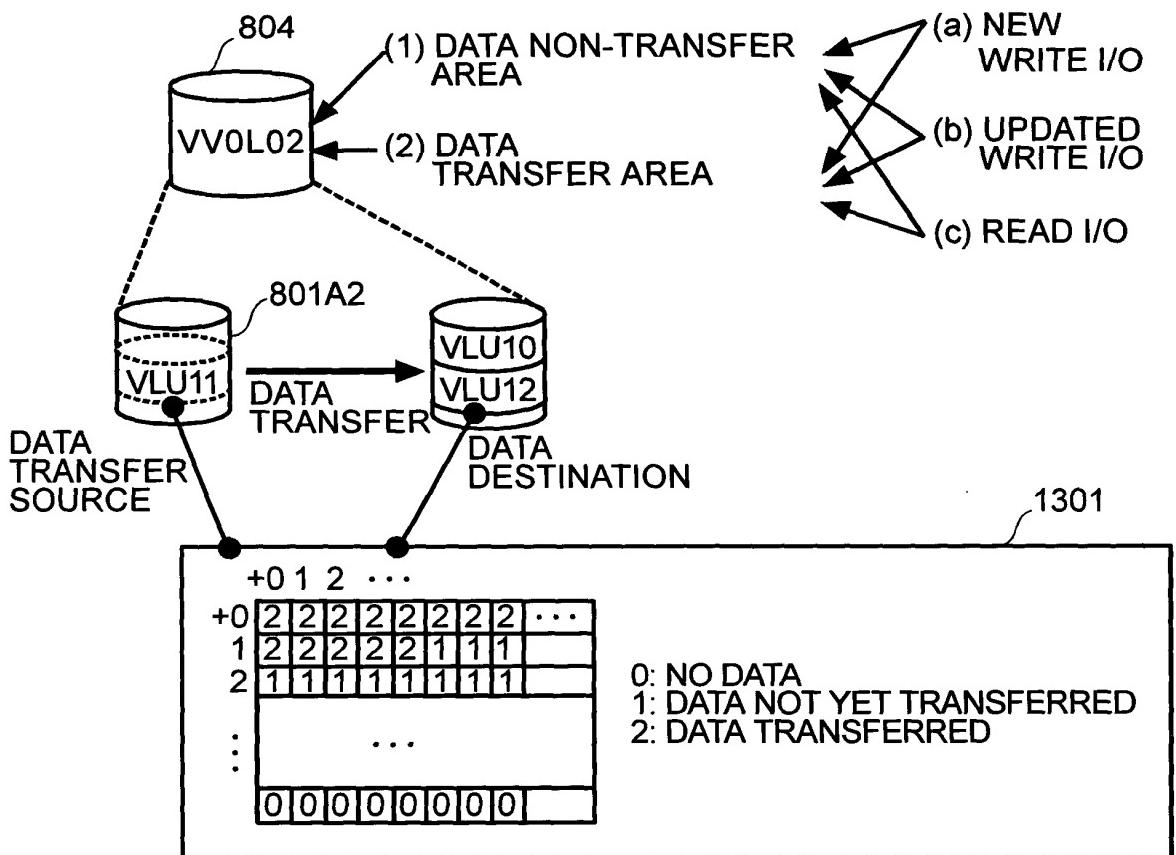
**FIG.11**



# FIG.12

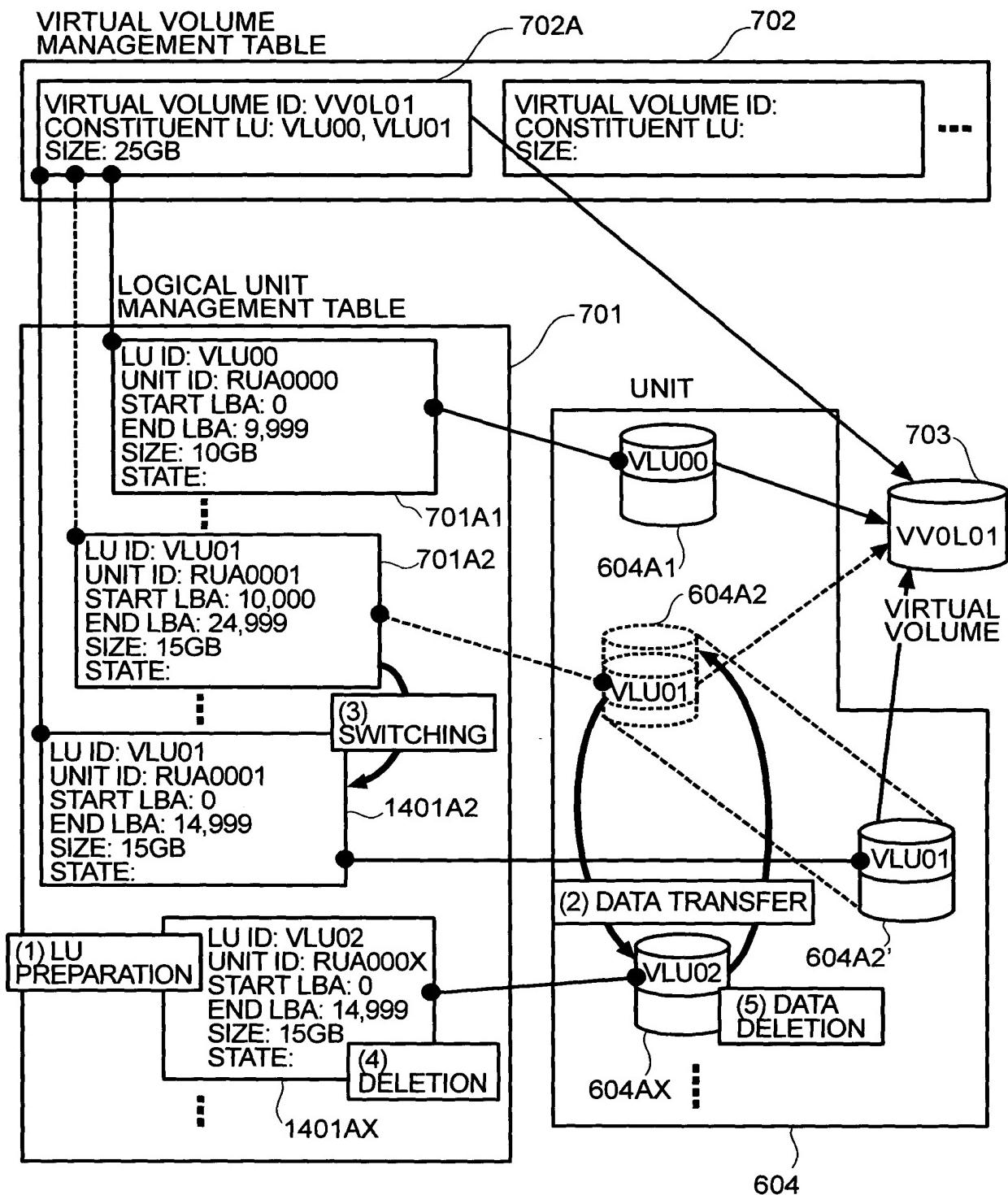


# FIG.13

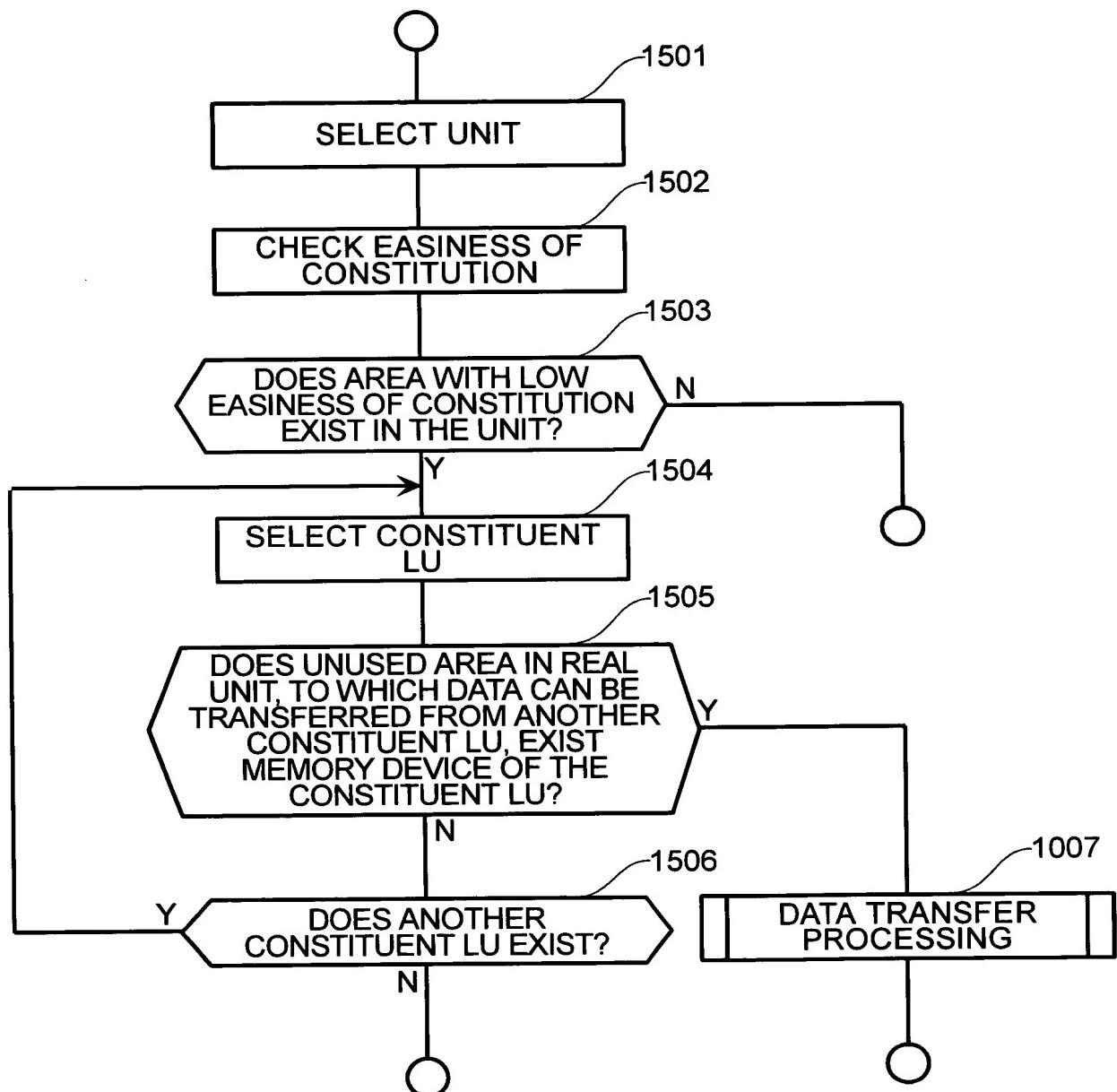


INPUT I/O	ACCESS DESTINATION	(2) DATA TRANSFER AREA		
		(1) DATA NON-TRANSFER AREA	DATA NOT YET TRANSFERRED (PERTINENT BLOCK BITMAP= 1)	DATA TRANSFERRED (PERTINENT BLOCK BITMAP= 2)
(a) NEW WRITE	TO PERTINENT AREA	TO PERTINENT AREA	TO BLOCK BITMAP =0 AREA OF DATA TRANSFER DESTINATION, BITMAP AFTER WRITE= 2	
(b) UPDATED WRITE	TO PERTINENT AREA		<ul style="list-style-type: none"> <li>TO DATA TRANSFER SOURCE OR</li> <li>DATA TRANSFER DESTINATION PERTINENT BLOCK BITMAP =2</li> </ul>	TO DATA TRANSFER DESTINATION
(c) READ	TO PERTINENT AREA	TO DATA TRANSFER SOURCE	TO DATA TRANSFER DESTINATION	

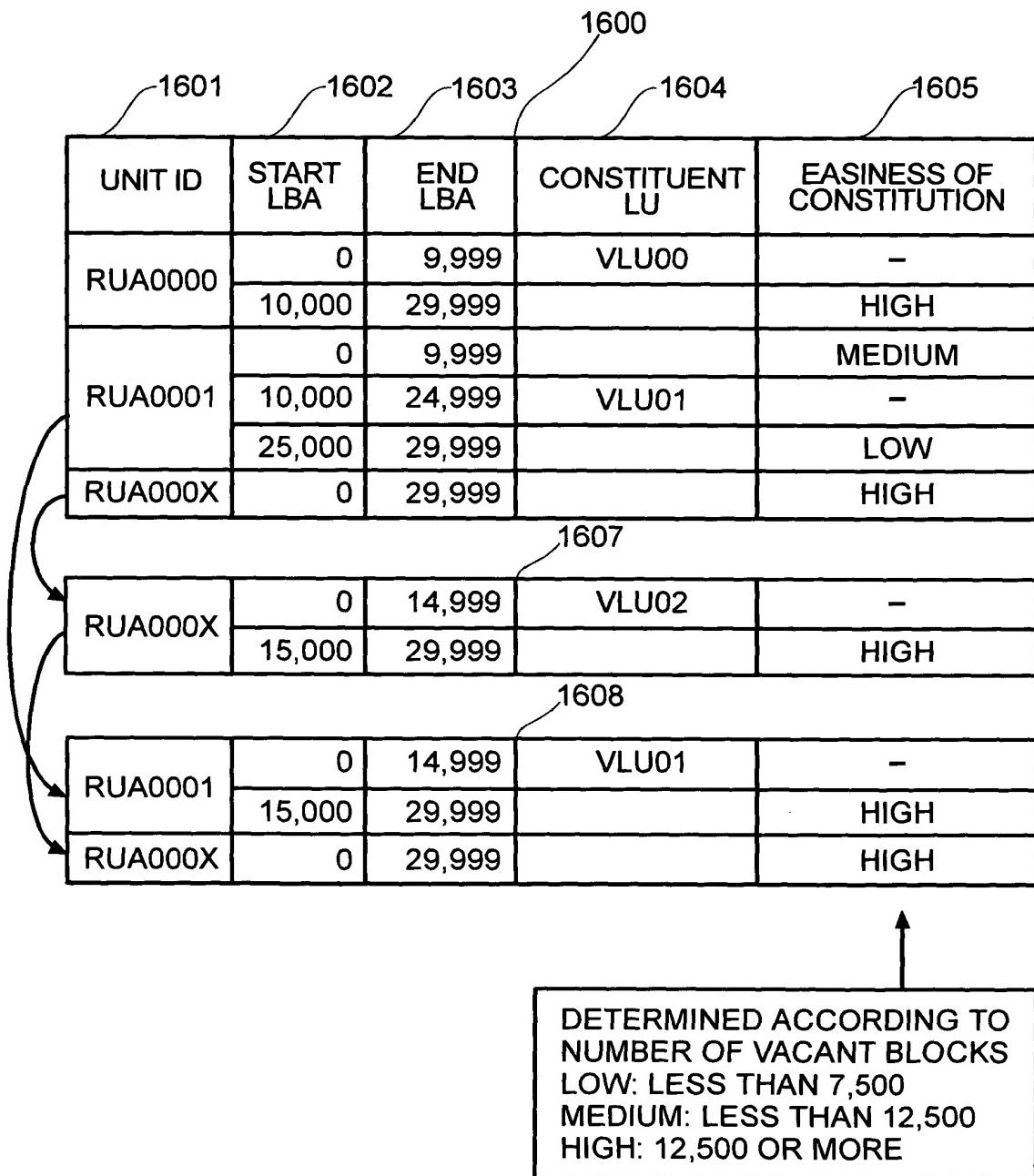
**FIG.14**



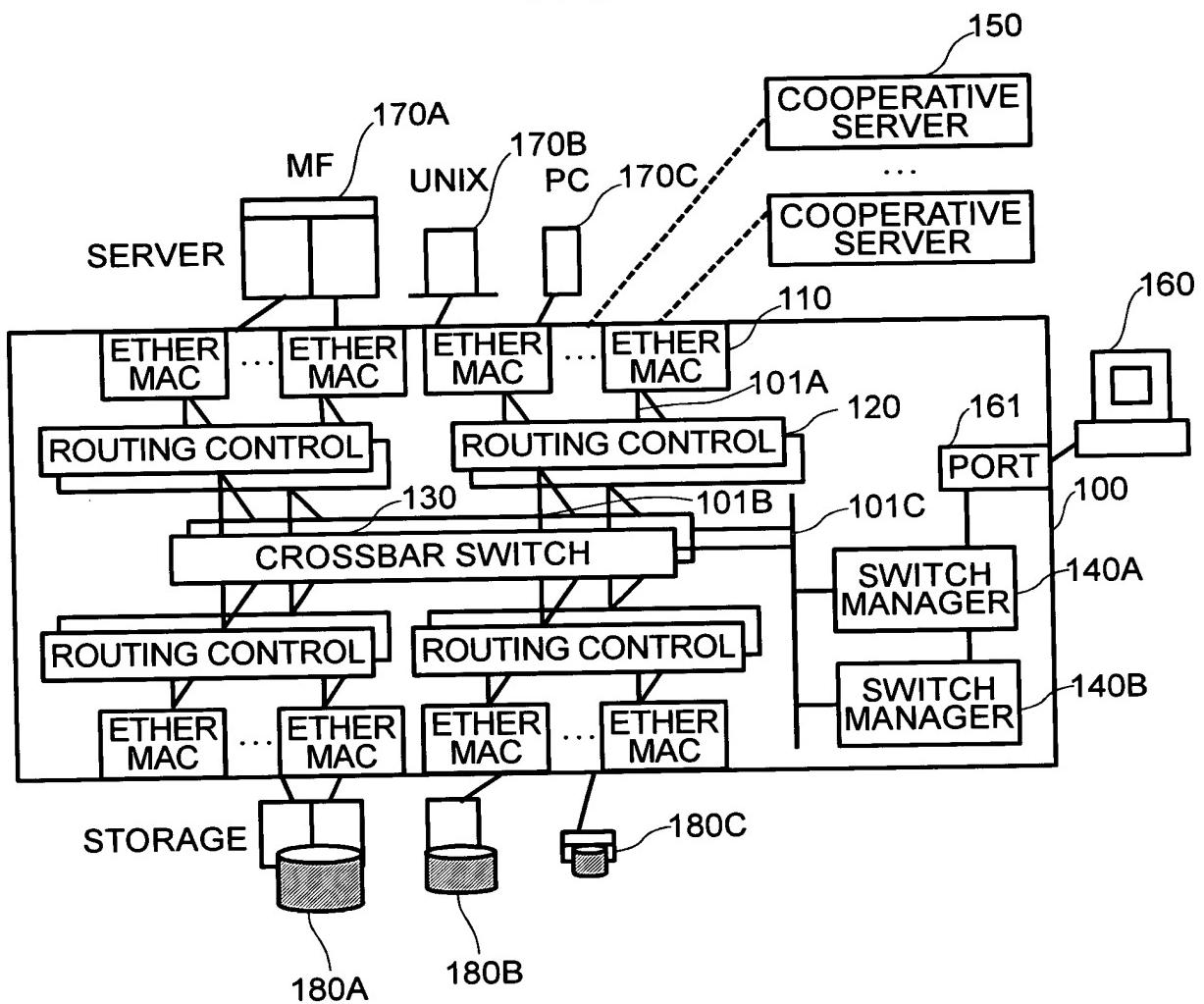
**FIG.15**



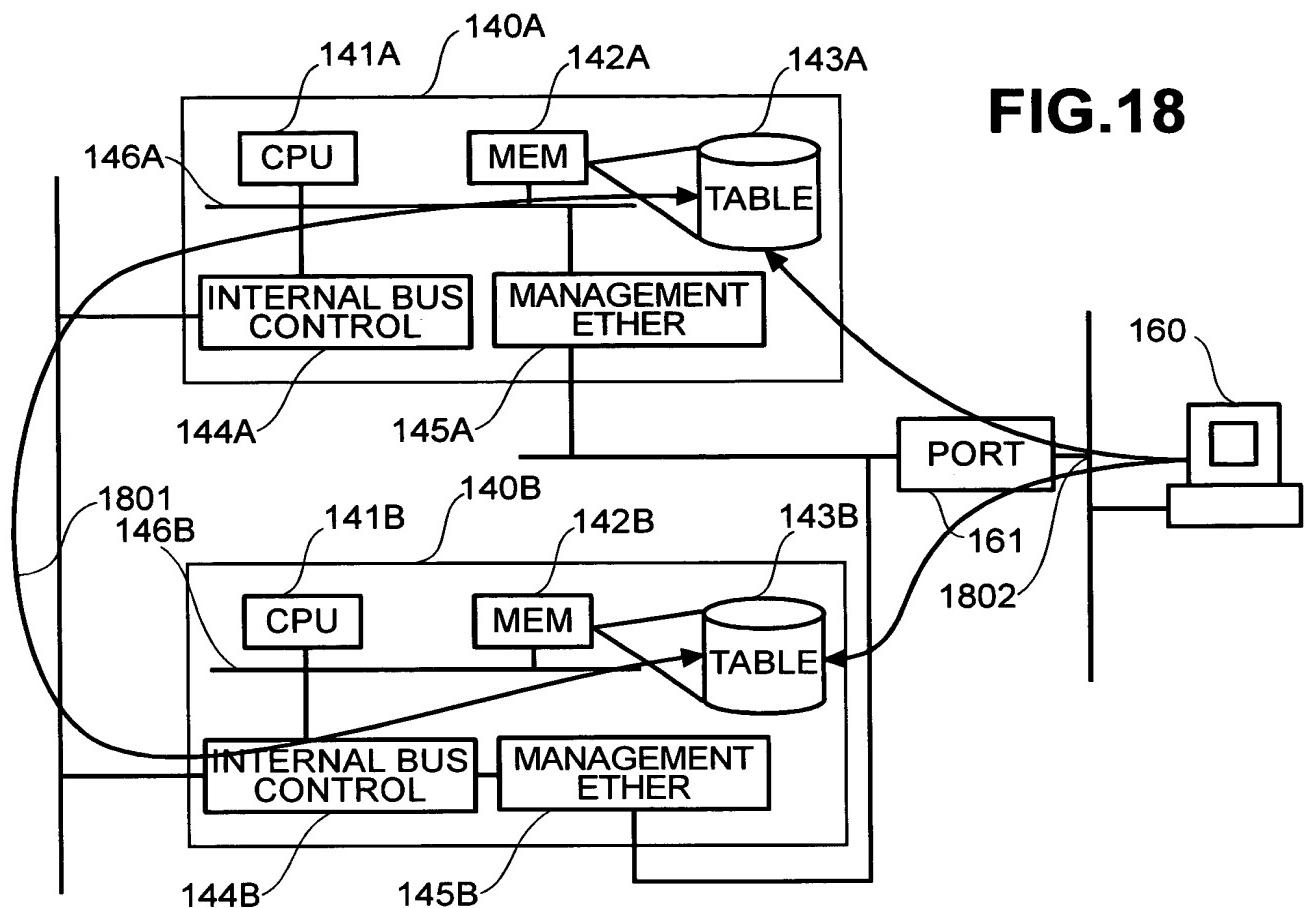
## FIG.16



# FIG.17



# FIG.18



# FIG.19

